

REMARKS

Please cancel Claim 13 without prejudice. Claims 1-12 and 14-29 are pending. Claims 1, 7, 8, 10, 12, 14-17, 19-26 and 29 are amended herein. No new matter is added as a result of the claim amendments.

102 Rejections

The instant Office Action states that Claims 1-12 and 14-29 are rejected under 35 U.S.C. § 102(e) as being anticipated by Myers et al. ("Myers;" US 6,185,127).

Independent Claim 1 recites that an embodiment of the present invention is directed to a method for implementing multiple functions in a device, "said device comprising a plurality of analog blocks interconnected in a single integrated circuit, wherein analog blocks in said plurality of analog blocks are selectively and electrically couplable to and decouplable from other analog blocks in said plurality of analog blocks, wherein said configuration information is for selectively and electrically coupling a particular combination of said analog blocks in a particular configuration according to an analog function to be performed." Claims 2-9 are dependent on Claim 1 and recite additional limitations.

Independent Claim 10 recites that an embodiment of the present invention is directed to a method for configuring a device, "said device comprising a first analog block comprising a plurality of analog elements having changeable characteristics, wherein said configuration information is for specifying characteristics of said analog elements according to an

analog function to be performed and wherein said configuration information is also for selectively and electrically coupling and decoupling said first analog block to and from a second analog block of said device according to said analog function.” Claims 11-12 and 14-25 are dependent on Claim 10 and recite additional limitations.

Independent Claim 26 recites that an embodiment of the present invention is directed to a method for configuring a device comprising a plurality of analog blocks, said method including “receiving configuration information into a configuration register of said device, wherein said configuration information is for causing a first analog block of said device to be selectively and electrically coupled to a second analog block of said device according to an analog function to be performed, said configuration information also for specifying analog characteristics of said first and second analog blocks and for selecting inputs and outputs of said first and second analog blocks.” Claims 27-29 are dependent on Claim 26 and recite additional limitations.

Thus, each and every claim recites analog blocks that can be selectively and electrically coupled to and decoupled from each other according to configuration information. Applicants respectfully submit that this feature is not shown or suggested by Myers.

Figure 1 of Myers shows a microprocessor supervisory device 10 that can become one of either a first, second, third or n configuration (elements 16-19 of Myers’ Figure 1). The elements 16-19 do not represent different

analog blocks that can be selectively and electrically coupled/decoupled from each other.

Figure 5 of Myers shows analog subcircuits 41, 42 and 43 and a logic gate 44. There is no showing or suggestion in Myers that subcircuits 41, 42 and 43 and logic gate 44 can be selectively and electrically coupled/decoupled from each other, particularly in response to configuration information. Furthermore, although Myers describes subcircuits 41, 42 and 43 and logic gate 44 as being integrated in the configurable microprocessor supervisory device 10, there is no showing or suggestion in Myers that the subcircuits 41, 42 and 43 and logic gate 44 are themselves configurable, particularly in response to configuration information. Thus, the subcircuits 41, 42 and 43 and logic gate 44 do not show or suggest the analog blocks of the present claimed invention.

Therefore, Applicants respectfully submit that Myers does not show or suggest the present claimed invention. Accordingly, Applicants respectfully submit that independent Claims 1, 10 and 26 traverse the basis for rejection under 35 U.S.C. § 102(e) and are in condition for allowance. As such, Applicants also respectfully submit that Claims 2-9, 11-12, 14-25 and 27-29 traverse the basis for rejection under 35 U.S.C. § 102(e), as these claims are dependent on allowable base claims and recite additional limitations.

Conclusions

In light of the above remarks, reconsideration of the rejected claims is respectfully requested.

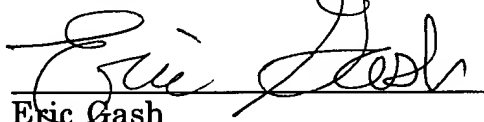
Based on the arguments presented above, it is respectfully asserted that Claims 1-12 and 14-29 overcome the rejections of record and, therefore, allowance of these claims is solicited.

The references cited but not relied upon have been reviewed. These references were not found to show or suggest the present claimed invention: US 5,574,678; US 5,905,398; US 6,141,376; and US 6,453,175.

The Examiner is invited to contact Applicants' undersigned representative if the Examiner believes such action would expedite resolution of the present Application.

Respectfully submitted,

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